

From: Chips JU Communication <communication@chips-ju.europa.eu>
Sent: petek, 06. september 2024 10:00
To: Polona Köveš
Subject: [BULK] Chips Pulse - September edition



Chips Pulse



SEPTEMBER

Welcome to the September edition of the Chips Pulse! This month, we're bringing you the latest updates on our calls and events, along with insightful articles about Chips Joint Undertaking (JU), its innovative projects, and the industry.

In this issue, we highlight the upcoming deadlines for several calls, as well as a reminder about the calls that opened earlier in the summer. The second phase of the ECS R&I calls, which include the Global IA and RIA calls according to SRIA 2024, is fast approaching, with the application deadline set for 17 September 2024. Additionally, the Chips for Europe Initiative calls, focusing on Competence Centres and support to the European Network of Chips Competence Centres, have a submission deadline of 2 October 2024.

We're also excited to feature several events, like the Online Networking Event for Competence Centres Applicants organised by the three Industry Associations (3As), a webinar on

Heterogeneous integration for Future High Speed Communication, and the European Conference on EDGE AI Technologies and Applications.

Stay tuned for more updates on upcoming calls and the registration for the EFECs event! Dive into this edition to stay informed about all the latest developments in the Chips JU community and beyond!



New calls have open

The **Chips Joint Undertaking** has launched two calls, as part of the **Chips for Europe Initiative**:

► **Pilot Line on Advanced Photonic Integrated Circuits (CPL5)**: This pilot line will serve as a critical infrastructure for bridging the gap between laboratory research and industrial-scale production, facilitating the development of reliable, scalable, and cost-effective Photonic Integrated Circuits (PIC) solutions.

Application deadline: 17 September 2024

► **Design Platform (CDP)**: The purpose of this call is to select a consortium that will be responsible for implementing, hosting and running the Design Platform and coordinating its development, operation, and maintenance as well as related user support activities, in close collaboration with the Chips Joint Undertaking.

Application deadline: 10 October 2024

[More information](#)



The deadline is approaching

“ ” . ”

ECS R&I

The second stage of the ECS R&I calls, open to the successful project proposals from the first stage, is closing soon:

- Global IA call according to SRIA 2024
- High-performance RISC-V Automotive Processors supporting SDV
- Service Oriented Framework for the Software Defined Vehicle of the future
- Global RIA call according to SRIA 2024
- Sustainable and greener manufacturing

Application deadline: 17 September 2024

[More information](#)

Chips for Europe Initiative

- Support to the European Network of Chips Competence Centres

► Competence Centres

Application deadline: 2 October 2024

[More information](#)

Coming soon

The documents for the next Chips JU Calls are already available for consultation. Applications can be submitted starting from 24 September 2024.

- Framework Partnership Agreements for developing Quantum Chip Technology for stability Pilot Lines
- Framework Partnership Agreements for developing Quantum Chip Technology for high-quality Trapped Ions Pilot Lines

[More information](#)

Events



3As Networking Event

10 September

[Registration](#)



HiConnects Webinar

10 September

[Registration](#)



European Microwave Week 2024

22-27 September

[Registration](#)



European Conference on EDGE AI Technologies and Applications

21-23 October

[Registration](#)

In case you missed it:

Have you seen the new video posted on our YouTube channel?

Check it out to learn more about the Chips JU and discover how we're driving innovation and shaping the future of semiconductors. [Watch it here.](#)

Quantum technologies - How can we lead in the industries of the future?

Follow the Euractiv Hybrid Conference to discuss the opportunities and challenges of quantum technologies for the EU. [Join on Youtube.](#)

EFECS (European Forum for Electronic Components and Systems) is back!

The event will take place at the ICC, in Ghent, Belgium, on 5-6 December, 2024. Registration will open soon!

Top News

Event

SEMI EuroPAT Workshop
on Sept 9-10 in Berlin

The EuroPAT Workshop, an annual event of the SEMI Integrated Packaging Assembly & Testing European Chapter

Calls

EU-Republic of Korea
partner for semiconductors and research





In June 2023, South Korea and the European Commission (EC) established the Korea-EU Digital

Pilot Lines

ARCTIC – Advanced Research on Cryogenic Technologies for Innovative Computing

The ARCTIC ("Advanced Research on Cryogenic Technologies for Innovative

<p>Technology Community (ESiPAT-TC)...</p> <p>READ NOW</p>	<p>Partnership to enhance cooperation and...</p> <p>READ NOW</p>	<p>Computing”) project aims to establish a complete and...</p> <p>READ NOW</p>
<p>Industry</p> <p>Chips and Pommes Frites: A French Semiconductor Success Story</p> <p>A review of the microwave and wireless industry in France. During this summer and early autumn, France’s capital city, Paris, will host two important global events.</p> <p>READ NOW</p>	<p>Industry</p> <p>European Commission approved a major German semiconductor manufacturing project</p> <p>The European Commission has authorised, under EU State aid regulations, a €5 billion German measure to back European...</p> <p>READ NOW</p>	<p>Industry</p> <p>Creating a European Chips Industry</p> <p>In September 2023, the EU Chips Act came into force. The Act aims to address a range of supply chain and strategic issues that have emerged since 2020 by building a sustainable...</p> <p>READ NOW</p>
<p>Event</p> <p>Semicon Summer School proves effective in attracting global talent to Eindhoven</p> <p>“The summer school is a one-stop shop to learn about various knowledge areas of semiconductor technology that helps students shape their further education.”</p> <p>READ NOW</p>	<p>Projects</p> <p>“Chip4Power” consortium established: Pilot line for microelectronics becomes operational</p> <p>The consortium, established in Rome at the CNR headquarters, will have the role of implementing body of the project that provides for the establishment, in the ...</p> <p>READ NOW</p>	<p>Industry</p> <p>Europe Is Convincingly Pursuing a Path to Semiconductor Sovereignty</p> <p>The push toward semiconductor sovereignty grows even more important as geopolitical factors, market turbulence, export controls and trade tensions threaten to upset the ...</p> <p>READ NOW</p>

[Chips Joint Undertaking](#)

Questions? Emails us at enquiries@chips-ju.europa.eu

[Unsubscribe](#)

Chips JU, Avenue Toison d'Or 56 -5/5,
1060, Brussels, Belgium

This email was sent to polona.koves@gov.si
Chips Joint Undertaking, Chips JU, TO 56 -5/5, Brussels, Brussels 1060, Belgium

[Unsubscribe](#)